FIG. 1A

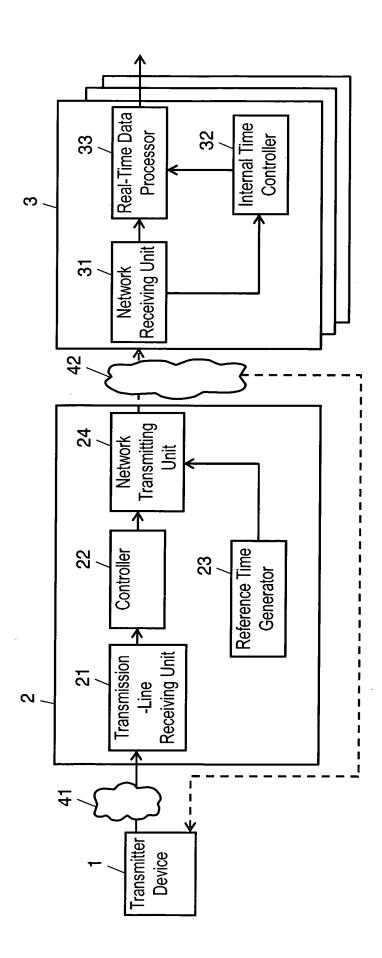


FIG. 2

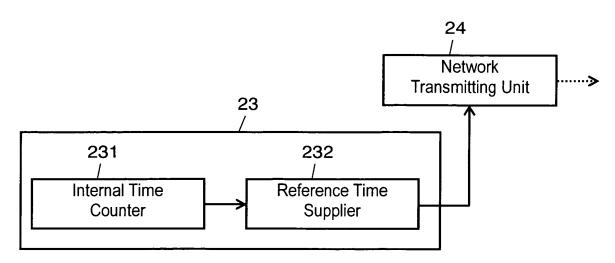


FIG. 3

332

Buffer

331

Extractor

Decoder

32

Internal Time Controller

FIG. 4

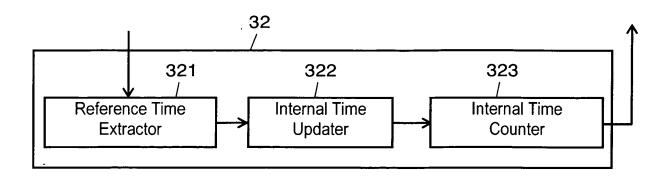


FIG. 5

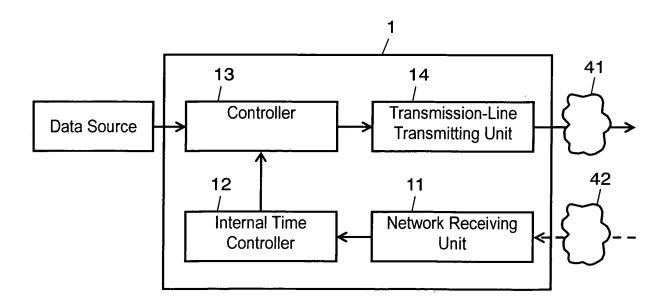


FIG. 6

